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GPIB.CONF IEEE488 library config file

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\* Syntax:

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\* interface { ... } starts new interface board section

\* device {...} device configuration

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/\* This section configures the configurable driver characteristics

\* for an interface board, such as board address, and interrupt level.

\* minor = 0 configures /dev/gpib0, minor = 1 configures /dev/gpib1, etc.

\*/

interface {

minor = 0 /\* board index, minor = 0 uses /dev/gpib0, minor = 1 uses /dev/gpib1, etc. \*/

board\_type = "ni\_usb\_b" /\* type of interface board being used \*/

name = "violet" /\* optional name, allows you to get a board descriptor using ibfind() \*/

pad = 0 /\* primary address of interface \*/

sad = 0 /\* secondary address of interface \*/

timeout = T3s /\* timeout for commands \*/

eos = 0x0a /\* EOS Byte, 0xa is newline and 0xd is carriage return \*/

set-reos = yes /\* Terminate read if EOS \*/

set-bin = no /\* Compare EOS 8-bit \*/

set-xeos = no /\* Assert EOI whenever EOS byte is sent \*/

set-eot = yes /\* Assert EOI with last byte on writes \*/

/\* settings for boards that lack plug-n-play capability \*/

base = 0 /\* Base io ADDRESS \*/

irq = 0 /\* Interrupt request level \*/

dma = 0 /\* DMA channel (zero disables) \*/

/\* pci\_bus and pci\_slot can be used to distinguish two pci boards supported by the same driver \*/

/\* pci\_bus = 0 \*/

/\* pci\_slot = 7 \*/

master = yes /\* interface board is system controller \*/

}

/\* Now the device sections define the device characteristics for each device.

\* These are only used if you want to open the device using ibfind() (instead

\* of ibdev() )

\*/

device {

minor = 0 /\* minor number for interface board this device is connected to \*/

name = "sme03" /\* device mnemonic \*/

pad = 28 /\* The Primary Address \*/

sad = 0 /\* Secondary Address \*/

eos = 0xa /\* EOS Byte \*/

set-reos = no /\* Terminate read if EOS \*/

set-bin = no /\* Compare EOS 8-bit \*/

}

device {

minor = 0

name = "scope"

pad = 8

sad = 0

}