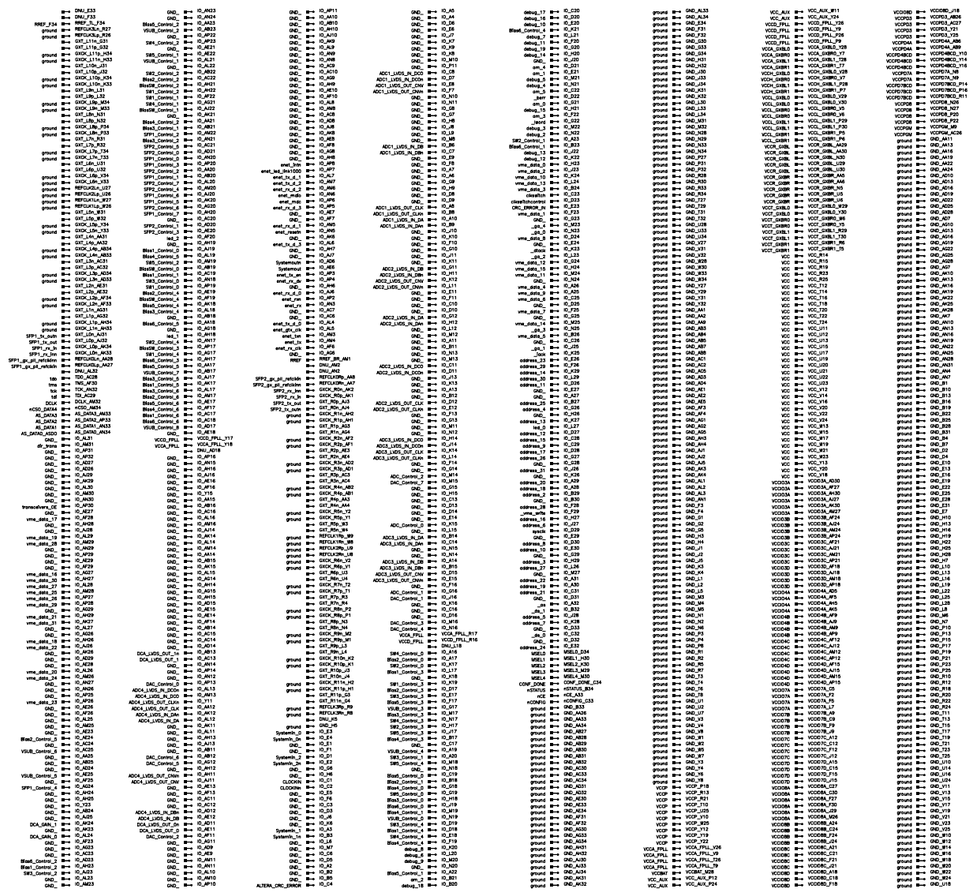
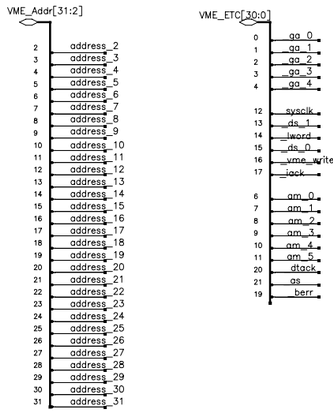
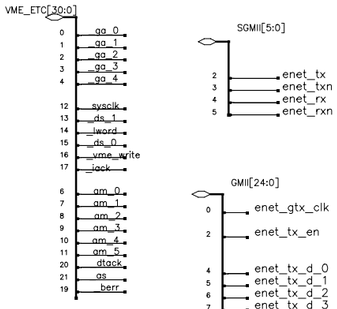
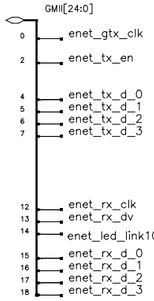
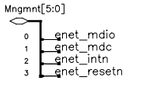
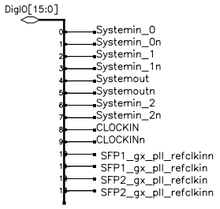


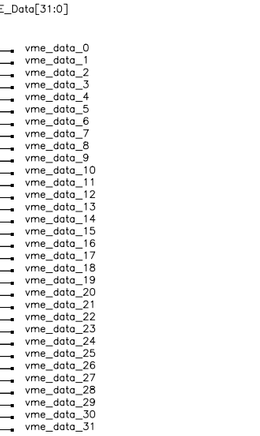


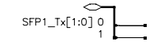


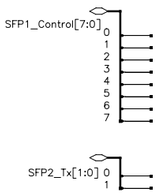


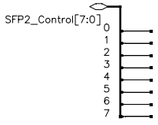


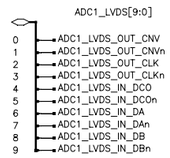


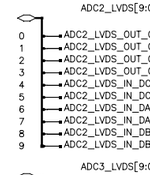


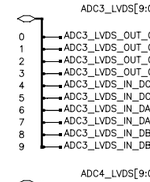


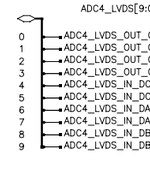


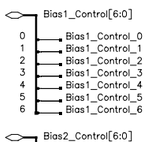


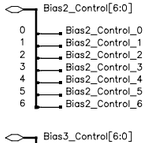


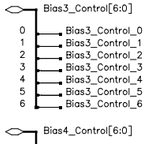


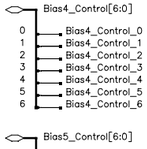


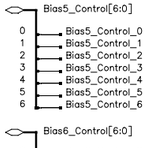


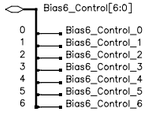


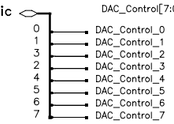


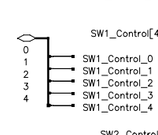


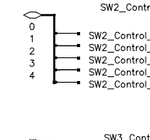


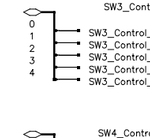


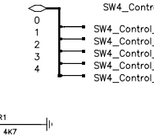


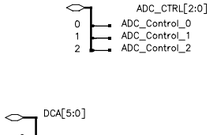


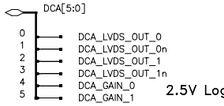


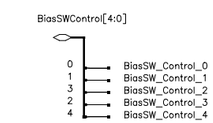


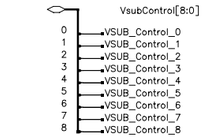


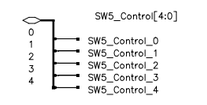


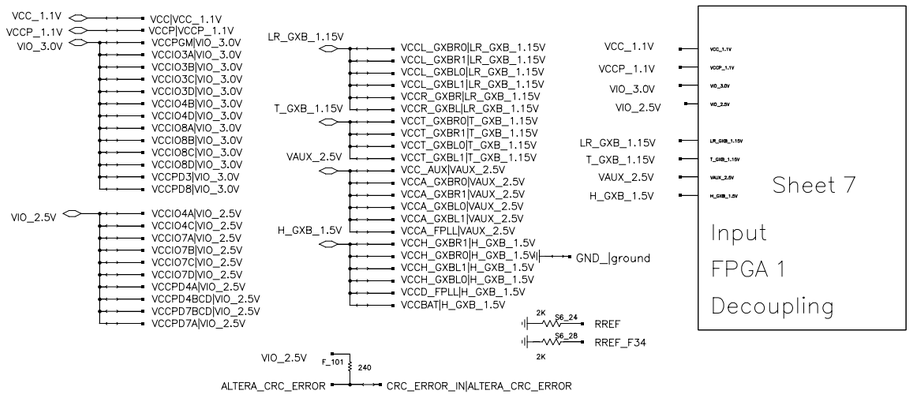


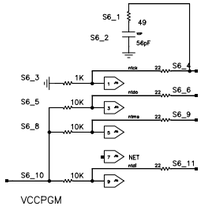


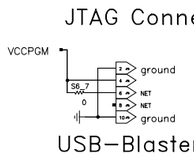


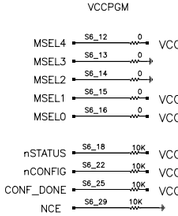


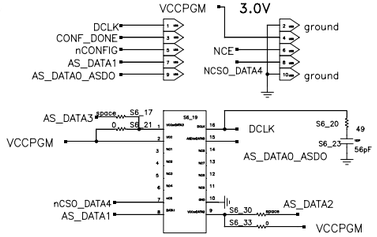


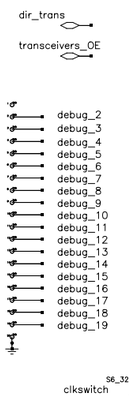


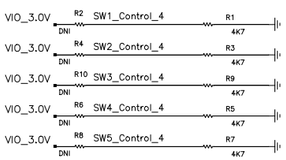






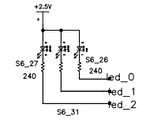






Notes:

All FPGA lines are 3.0V Logic unless specified.



Engineer: M. Bogdan	The University of Chicago		
Drawn by: M. Bogdan	Acquisition and Control Module		
DATE: 7/28/22	DAMIC FPGA		
SPC#3019 ASM#3020	REV. A	DRW. 3018	Sheet 6