



CL32Y106KCVZNWE
10uF/100V/1210

C5750X7S2A226M280KB
22uF/100V/2220

Firmware Note:
Make sure TPSA33 is not enabled before AD5293 is powered up.

Note
The +2V can be changed down to 0V. This will change the upper limit of the Bias Generator.
To replace +2V with 0V, remove S181_34 and Change value of S181_35 to 0 Ohm

Engineer: M. Bogdan	The University of Chicago
Drawn by: M. Bogdan	DAMIC - ACM
Date: 7/25/22	Bias Generator (-12V; +0.25V)
SPC83018 ASB/SCD	REV. A DRW. 3018 Sheet 18_2