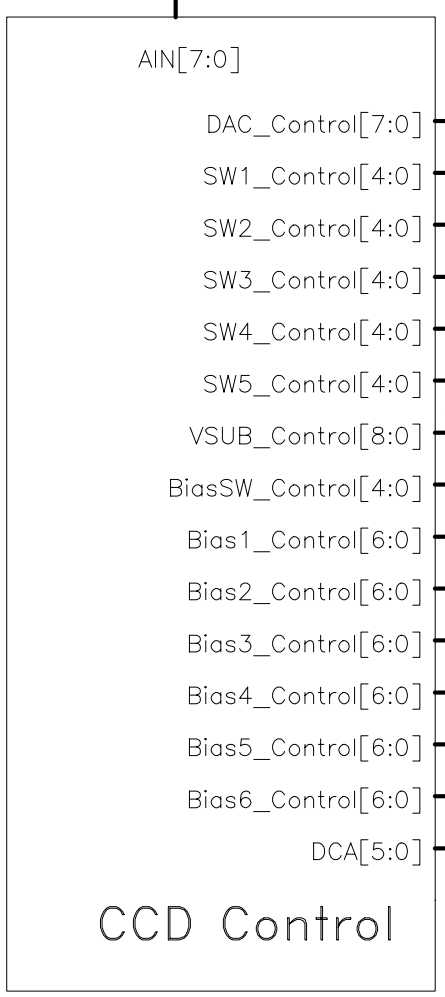
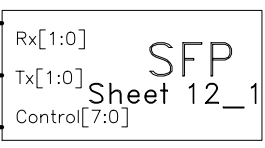
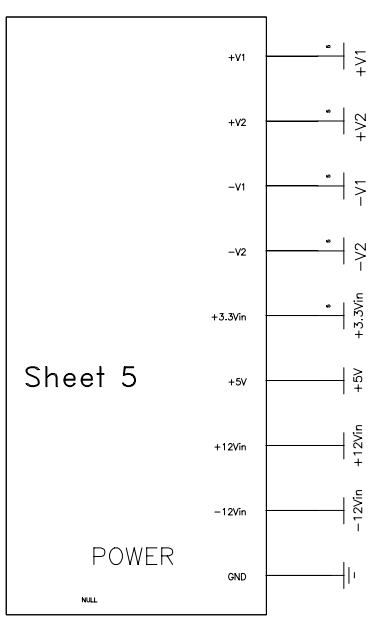
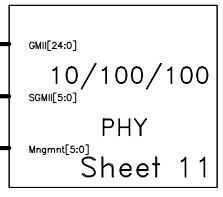
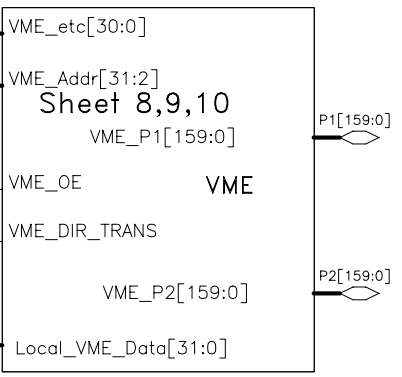
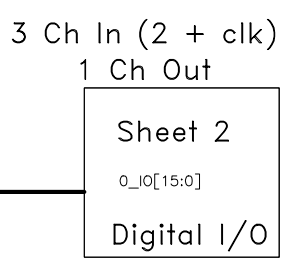
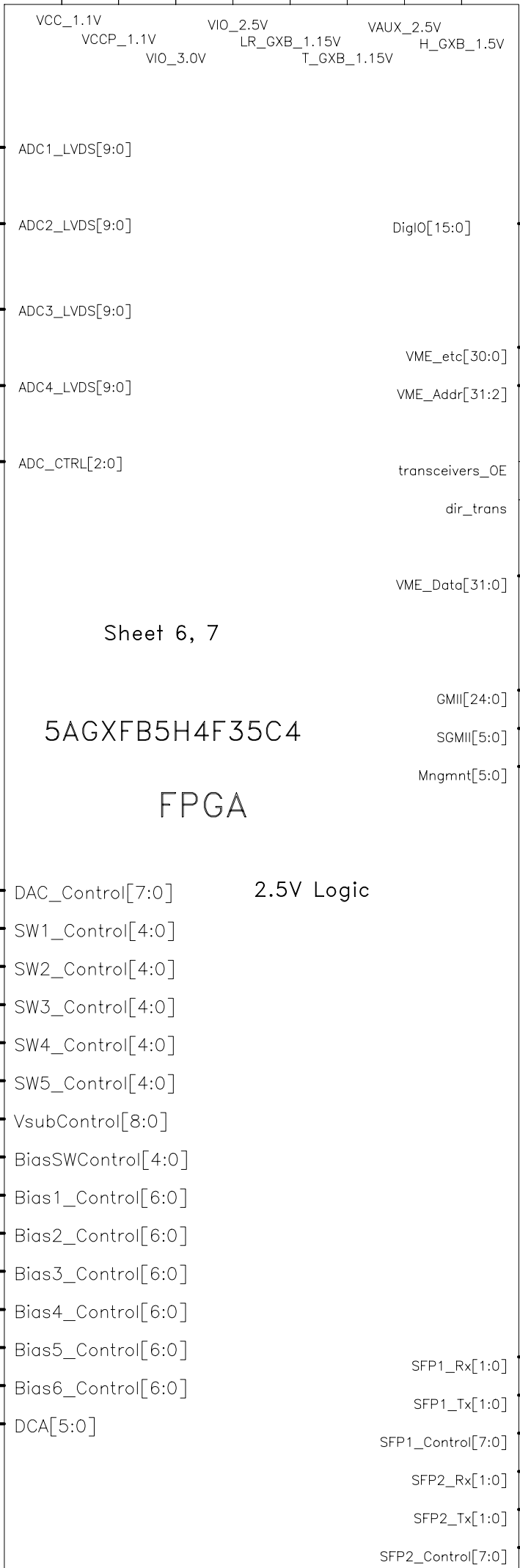
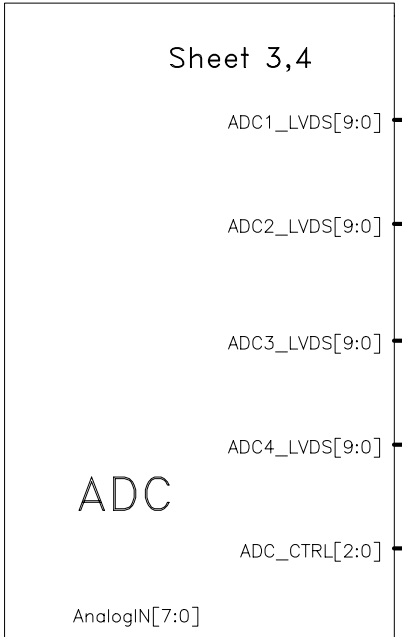


VCC\_1.1V VCCP\_1.1V VIO\_3.0V VIO\_2.5V LR\_GXB\_1.15V T\_GXB\_1.15V VAUX\_2.5V H\_GXB\_1.5V



Sheet 13, 14, 15, 16, 17, 18, 19

Note: +V1, +V2, -V1, -V2 are User Defined VME Power Rails  
 +5V, +3.3V, +12V, -12V are Standard VME Power Rails

Engineer: M. Bogdan	The University of Chicago Acquisition and Control Module DAMIC Top Level
Drawn by: M. Bogdan	
DATE: 7/27/22	
SPC#3019 ASM#3020	REV. A   DRW. 3018   Sheet 1